

What is claimed is:

1. A method for adjusting an active filter, comprising:  
providing a capacitor for determining a frequency response;  
ascertaining a measure of the frequency response and a prescribable nominal frequency response from a memory arrangement;  
selecting an adjustment parameter from a plurality of adjustment parameters stored in the memory arrangement based on the measure of the frequency response and the prescribable nominal frequency response; and  
adjusting an adjustable capacitor on the basis of the selected adjustment parameter.
2. The method according to claim 1, wherein ascertaining the measure of the frequency response of the active filter is a time constant.
3. The method according to claim 2, wherein ascertaining the time constant is normalized to a particular value of a clock frequency used.
4. The method according to claim 1, wherein ascertaining from the memory arrangement uses an addressable table memory whose address has a word length comprising at least two digits, and at least one digit in the address word length is filled with a digital value derived from the ascertained measure of the frequency response, and at least one digit in the address word length is filled with a digital value which describes the prescribed nominal frequency response.

5. A circuit arrangement for filtering an electrical signal, comprising:  
an active filter including at least one adjustable capacitor that determines frequency response;  
a circuit to determine a measure of the frequency response of the active filter, the circuit including a memory arrangement which can store a plurality of adjustment parameters for adjusting the at least one adjustable capacitor; and  
a control device which is set up to take an ascertained measure of the frequency response and a nominal frequency response set on the circuit arrangement as a basis for selecting a stored adjustment parameter and adjusts the at least one adjustable capacitor on the basis of the selected adjustment parameter.

6. The circuit arrangement according to claim 5, wherein the at least one adjustable capacitor includes at least one invariable base capacitor and an adjustable capacitor component.

7. The circuit arrangement according to claim 5, wherein the at least one adjustable capacitor includes a plurality of series circuits connected in parallel, which each one of the at least one adjustable capacitor comprise a single capacitor and a switch.

8. The circuit arrangement according to claim 5, wherein the memory arrangement includes a table memory which can store digital values in binary form as adjustment parameters.

9. The circuit arrangement according to claim 8, wherein the table memory includes an address word length having at least two digits, and the circuit arrangement fills at least one digit in the address word length with a digital value derived from the ascertained measure of the frequency response and fills at least one digit in the address word length with a digital value which describes the prescribed nominal frequency response.

10. The circuit arrangement according to claim 5, wherein the circuit arrangement is arranged to determine a time constant as a measure of the frequency response.

11. The circuit arrangement according to claim 10, wherein the circuit arrangement is arranged to normalize the ascertained time constant to a particular value of a clock frequency used when ascertaining the time constant.

12. The circuit arrangement according to claim 5, wherein the components of the circuit arrangement are integrated into a semiconductor.

13. The circuit arrangement according to claim 5, further including at least one reference capacitor having a value in a known ratio to the value of at least one capacitor which determines frequency response, wherein the circuit for determining a measure of the frequency response of the filter is arranged to ascertain a measure of the value of the reference capacitor, and the control device is arranged to use the measure of the value of the reference capacitor instead of the measure of the frequency response of the active filter.

14. The circuit arrangement according to claim 13, wherein the reference capacitor and the capacitor which determines frequency response are made up of standard capacitors of the same value.